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PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date

January 17, 2007

Jennifer A. Steele

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Stephen J. Gualandri and
Theodore T. Pekny

Attorney Docket No.: 501113.02

Patent No. : US 6,868,016 B2

Certificate

JAN 21 2007

Issued : March 15, 2005

of Correction

Title : HIGH VOLTAGE POSITIVE AND NEGATIVE TWO-PHASE DISCHARGE SYSTEM
AND METHOD FOR CHANNEL ERASE IN FLASH MEMORY DEVICES

NOTIFICATION OF ERRORS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following errors were noted in a review of the above-identified letters patent. One of these errors was inadvertently made in the original application, while the others occurred in the printing of the patent. Since the errors are of an obvious nature, a formal Certificate of Correction is not believed to be warranted at this time. Therefore, applicants request that this notification be placed in the Patent and Trademark Office file.

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Column 1, Line 42	“memory-cell 100,”	--memory cell 100,--
Column 2, Line 27	“having enough kinetic”	--have enough kinetic--
Column 3, Line 37	“affect of the capacitive”	--effect of the capacitive--

Column 4, Line 20	“undesirable affects”	--undesirable effects--
Column 4, Line 42	“invention, a”	--invention, an--
Column 5, Line 2	“memroy and the affect”	--memory and the effect--
Column 7, Line 14	“turn ON,”	--turns ON,--
Column 7, Line 17	“those of conventional”	--those of a conventional--
Column 8, Line 23	“at time T22”	--a time T2--
Column 9, Line 17	“a an interface”	--as an interface--
Column 10, Line 48	“a digital cameras,”	--digital cameras,--
Column 11, Line 57	“to a n-channel”	--to an n-channel--

Respectfully submitted,

Date: Jan. 16, 2007

By:


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Enclosure:
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